

AMENDMENTS TO THE SPECIFICATION

Please replace paragraph [0015] of the originally filed specification with the following:

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[0015] Figure 2 shows a system in accordance with one embodiment of the invention. More specifically, Figure 2 shows a multiprocessor system ~~[[(100)]]~~ (140) having a node topology in accordance with one embodiment of the invention. In the embodiment shown in Figure 2, each node (*i.e.*, Node A (150), Node B (152), Node C (154), Node D (156), Node E (158)) in the node topology is connected to every other node. This node topology allows each node multiple communication pathways to communicate with any other node, such that if one or more nodes fail, the remaining nodes may still be able to communicate with one another. In one embodiment of the invention, a mesh interconnect (142) provides the communication hardware infrastructure (*i.e.*, the hardware to physically connect each of the multiple nodes in the node topology) for the node topology used within the system. Those skilled in the art will appreciate that other node topologies may be used that provide multiple communication pathways between each of the nodes in the node topology without requiring every node to be directly connected to every other node, as shown in Figure 2.

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